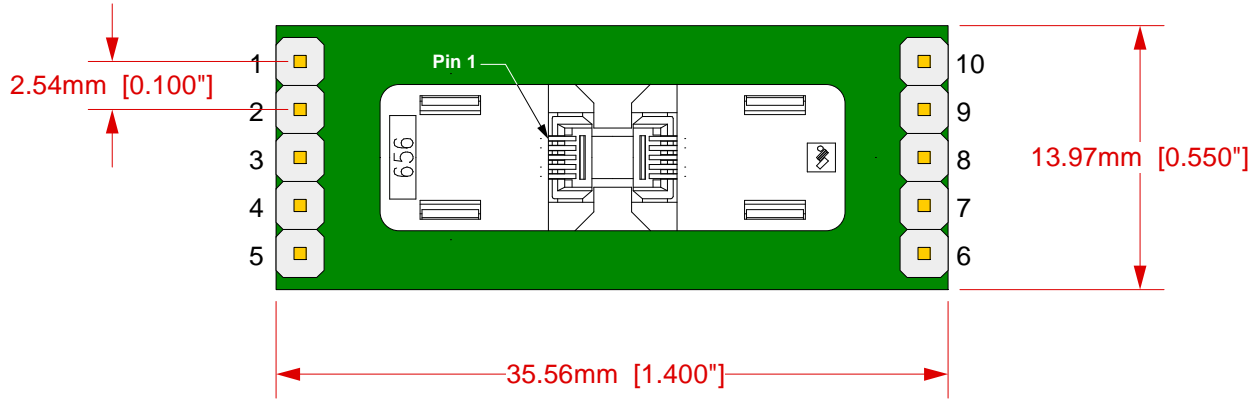
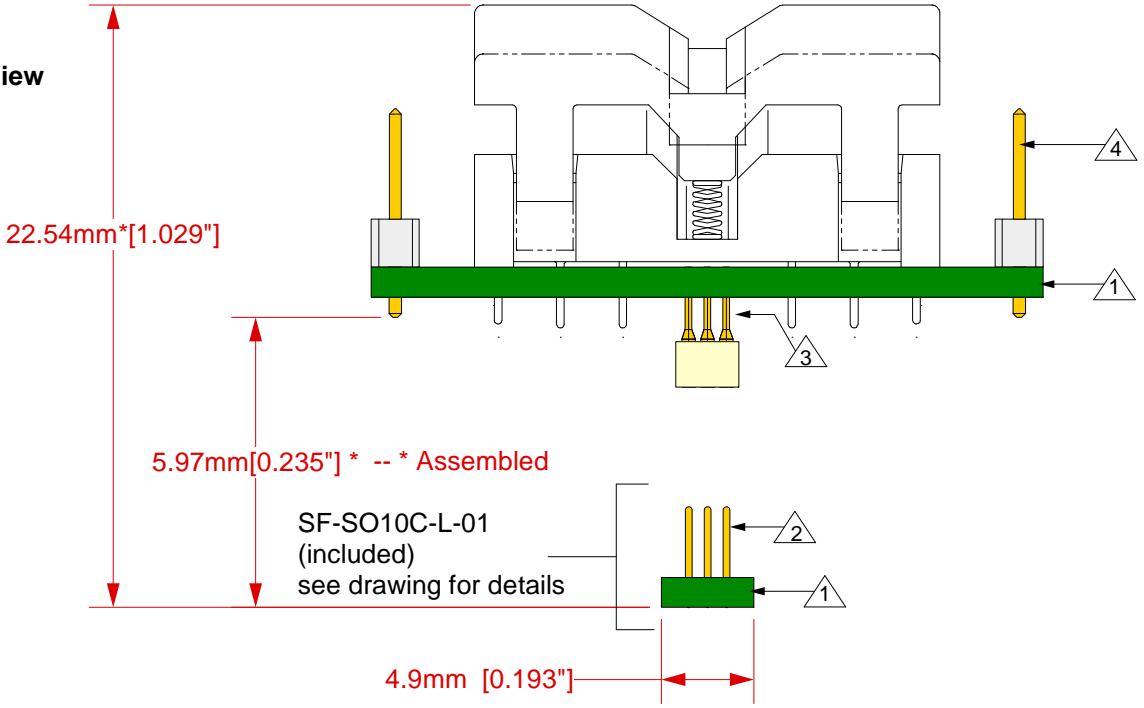


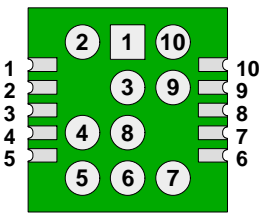
Top View



Side View



Surface mount Foot Map (top View)




NOT TO SCALE

- 1 Substrate: 0.0625 \pm 0.007" per IPC 4101/21/26/83/98, Td \ge 345C 1/2 oz. Cu clad. RoHS finish
- 2 Pins: Material- Brass Alloy 360 1/2 hard; finish- 10 μ " Au over 50 μ " Ni (min.).
- 3 Pins: shell material- Brass Alloy 360 1/2 hard; finish- 10 μ " Au over 50 μ " Ni (min.). Contact material- BeCu; finish 10 μ " Au over 100 μ " Ni (min.)
- 4 Test points: material- Phosphor Bronze; plating- Sn over 50 μ " Ni. Gold flash on contact end.

Description: Carrier Adaptor, SOIC
 10 position ZIF socket with test points to 10 position surface mount foot, 0.5mm pitch (leadless). Two piece assembly has gold plated pluggable interface between socket and foot assemblies. Pin mapping is 1:1



Tolerances: diameters \pm 0.03mm [\pm 0.001"], PCB perimeters \pm 0.13mm [\pm 0.005"], PCB thicknesses \pm 0.18mm [\pm 0.007"], pitches (from true position) \pm 0.08mm [\pm 0.003"], all other tolerances \pm 0.13mm [\pm 0.005"] unless stated otherwise. Materials and specifications are subject to change without notice.

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	<p>Drawing: E Smolentseva</p>	<p>Date: 7/14/10</p>		<p>Modified:</p>
	<p>File: CA-SO10C-L-Z-T-01 Dwg.mcd</p>			