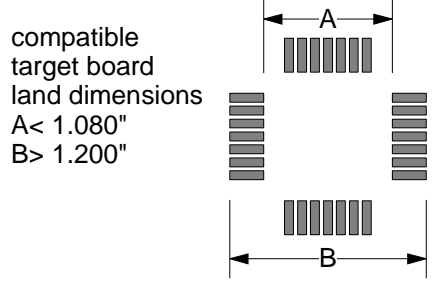



- 1 Substrate: 1.59mm ±0.18mm [0.0625" ±0.007"] FR4/G10 or equivalent high temp material. 17µm [1/2 oz.] Cu clad. SnPb plating.
- 2 Pins: material- Brass Alloy 360 1/2 hard; finish- 0.25µm [10µ"] Au over 1.27µm [50µ"] Ni (min.).



Description: **QFP Surface mount emulator Foot**
160 position mini-grid terminals to surface mount edge pads.

Tolerances: diameters ±0.03mm [±0.001"], PCB perimeters ±0.13mm [±0.005"], PCB thicknesses ±0.18mm [±0.007"], pitches (from true position) ±0.08mm [±0.003"], all other tolerances ±0.13mm [±0.005"] unless stated otherwise.

	SF-QFE160SA-L-01 Drawing © 1994 IRONWOOD ELECTRONICS, INC. PO BOX 21151 ST. PAUL, MN 55121 Tele: (651) 452-8100 www.ironwoodelectronics.com	Status: Released	Scale: 7:1	Rev: E
		Drawing: M. Tully		Date: 4/1/94
		File: SF-QFE160SA-L-01 Dwg	Modified: 4/13/00	