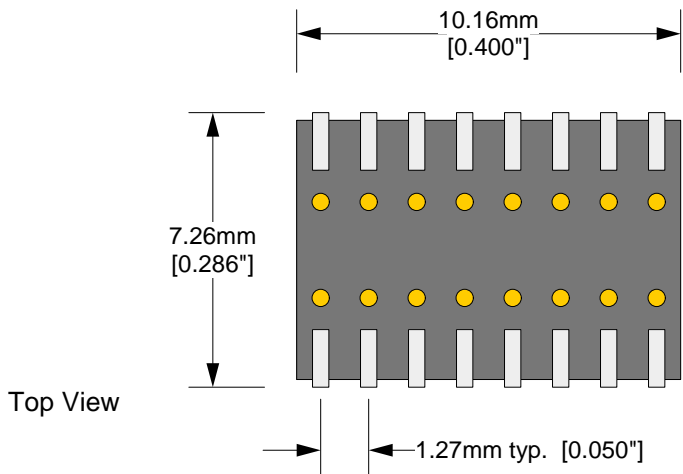
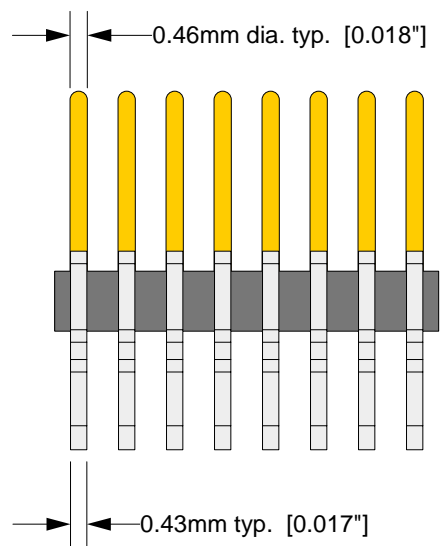


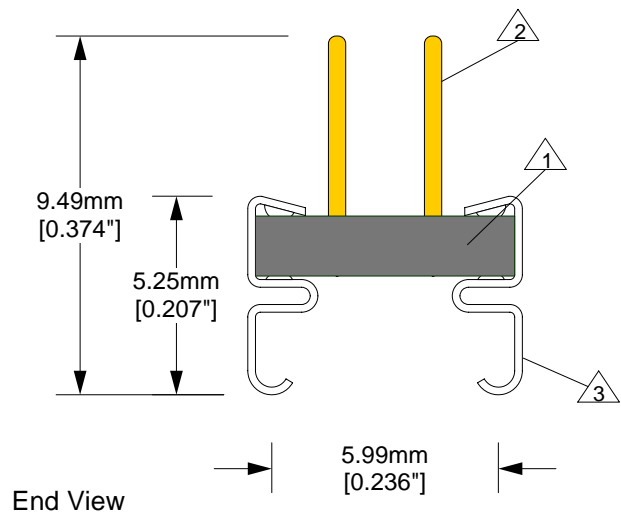
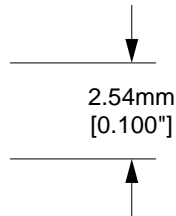
RoHS COMPLIANT



Top View

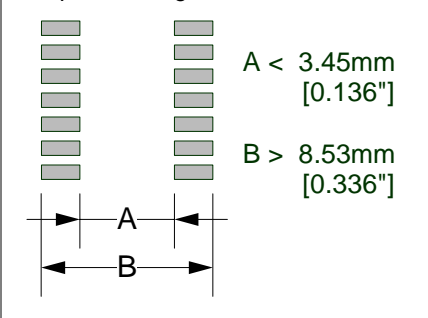


Side View



End View

Compatible Target Board Land Pattern




- 1 Substrate: 1.59mm \pm 0.18mm [0.0625" \pm 0.007"] per IPC 4101/21; iS410 - T =180°, T =350° RoHS plating; 8-16 μ m immersion silver.
- 2 Pins: material- Brass Alloy 360 1/2 hard; finish- 0.25 μ m [10 μ "] Au over 1.27 μ m [50 μ "] Ni (min.).
- 3 Leads: material- Phosphor bronze pre-plated pure tin finish. solder- Sn96Ag4 solid no flux.

Description: SO surface mount emulator foot

16 position (0.05" pitch) J-leaded surface mountable emulator foot, with Mini grid array (MGA) pin interface.

Tolerances: diameters \pm 0.03mm [\pm 0.001"], PCB perimeters \pm 0.13mm [\pm 0.005"], PCB thicknesses \pm 0.18mm [\pm 0.007"], pitches (from true position) \pm 0.08mm [\pm 0.003"], all other tolerances \pm 0.13mm [\pm 0.005"] unless stated otherwise.

	SF-SO16A-J-01F Drawing	Status: Released	Scale 5:1	Rev: A
	© 2007 IRONWOOD ELECTRONICS, INC. 11351 Rupp Drive, Suite 400, Burnsville, MN 55337 Tele: (952) 229-8200 www.ironwoodelectronics.com	Drawing: J. Glab		Date: 3/27/07
		File: SF-SO16A-J-01F Dwg		Modified: