

Substrate: 1.59mm ±0.18mm [0.0625" ±0.007"] FR4/G10 or equivalent high temp material. 17µm [1/2 oz.] Cu clad.

SnPb plating. Pins: shell material- 0.0035±0.0003 BeCu #172 1/2 hard;

<u>2</u> finish- 0.25μm [10μ"] Au over 1.27μm [50μ"] Ni (min.). Description: Probe Board for TEKTRONIX TLA700 series Logic Analyzer

680 position BGA land socket to 680 position surface mountable BGA emulator foot, with latch housing high density Mictor connectors on the Probe Board. Pin assignment/ interconnect is configured for the XILINX FG680 IC and Tektronix TLA 700 series logic analyzer.

Tolerances: diameters  $\pm 0.03$ mm [ $\pm 0.001$ "], PCB perimeters  $\pm 0.13$ mm [ $\pm 0.005$ "], PCB thicknesses  $\pm 0.18$ mm [ $\pm 0.007$ "], pitches (from true position)  $\pm 0.08$ mm [ $\pm 0.003$ "], all other tolerances  $\pm 0.13$ mm [ $\pm 0.005$ "] unless stated otherwise. Materials and specifications are subject to change without notice.

PB-BGA-FG680-01 Drawing	Status: Released	Scale:	2:1	Rev: C
© 2001 IRONWOOD ELECTRONICS, INC. PO BOX 21151 ST. PAUL, MN 55121 Tele: (651) 452-8100 www.ironwoodelectronics.com	Drawing: W. Watson		Date: 5/15/01	
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