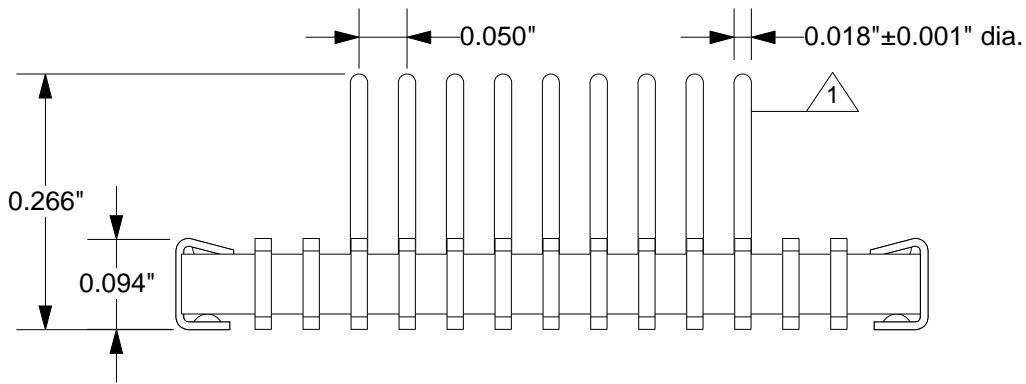


Top View




Side View

- 1 Substrate: 0.0625"±0.007" FR4/G10 or equivalent high temp material.
- 2 Pins: shell material- Brass; finish- 10µ" gold over 50µ" nickel (min.).

Description: Surface Mount PLCC emulator foot (low profile)  
 52 position, 0.05" pitch leads to mini-grid array

All tolerances: ±0.005" (unless stated otherwise). Materials and specifications are subject to change without notice.

<b>SF-PLCC52-J-05 Drawing</b>		Status: Released	Scale: 2:1	Rev: A
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	File: SF-PLCC52-J-05 Dwg, MC			ECO#