


- 1 Substrate: 1.59mm ±0.18mm [0.0625" ±0.007"] per IPC 4101/21, silver surface finish.
- 2 Pins: material- Brass Alloy 360 1/2 hard; finish- 0.25µm [10µ"] Au over 1.27µm [50µ"] Ni (min.).
- 3 Leads: material- BeCu Alloy 194; plating- 60/40 SnPb 3.80-10.16µm [150-400µ"].

Description: SO surface mount emulator foot
18 position J-led surface mountable emulator foot, with Mini grid array (MGA) pin interface.

Tolerances: diameters ±0.03mm [±0.001"], PCB perimeters ±0.13mm [±0.005"], PCB thicknesses ±0.18mm [±0.007"], pitches (from true position) ±0.08mm [±0.003"], all other tolerances ±0.13mm [±0.005"] unless stated otherwise.

SF-S018A-J-01F Drawing		Status: Released	Scale: 2:1	Rev: A
 <p>© 2007 IRONWOOD ELECTRONICS, INC. 11351 Rupp Drive, Suite 400, Burnsville, MN 55337 Tele: (952) 229-8200 www.ironwoodelectronics.com</p>	Drawing: E Smolentseva		Date: 02/12/07	
	File: SF-SO18A-J-01F Dwg			Modified: