


- △ 1 Substrate: 1.59mm ±0.18mm [0.0625" ±0.007"] FR4/G10 or equivalent high temp material. 17µm [1/2 oz.] Cu clad. SnPb plating.
- △ 2 Leads: material- BeCu Alloy 194; plating- 60/40 SnPb 3.80-10.16µm [150-400µ"].
- △ 3 Test points: material- Phosphor Bronze; plating- Sn over 1.27µm [50µ"] Ni. Gold flash on contact end.

Description: **CARRIER ADAPTOR:**
8 pin SOIC Zif socket to 8 pin SOIC J-ledged foot

Tolerances: diameters ±0.03mm [±0.001"], PCB perimeters ±0.13mm [±0.005"], PCB thicknesses ±0.18mm [±0.007"], pitches (from true position) ±0.08mm [±0.003"], all other tolerances ±0.13mm [±0.005"] unless stated otherwise.

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				Drawing: Ila Pal		Date: 6/26/97
				File: CA-SO8A-Z-J-T-01 Dwg		Modified: 5/11/00