





Substrate: 1.59mm ±0.18mm [0.0625" ±0.007"] per IPC 4101/21, gold surface finish.

Substrate: 3.18mm ±0.18mm [0.125" ±0.007"] FR4/G10 or equivalent high temp material. Non-clad.



Leads: material- phosphor bronze; plating- Sn96 Ag4 solid



Pins: shell material- Brass Alloy 360 1/2 hard; finish- 0.25µm [10µ"] Au over 1.27µm [50µ"] Ni (min.). Contact material- BeCu; finish 0.25µm [10µ"] Au over 2.54µm [100µ"] Ni



Test points: material- Phosphor Bronze; plating-Sn over 1.27µm [50µ"] Ni. Gold flash on contact

## **ROHS COMPLIANT**

**Description:** Carrier Adaptor (CA) 44 position SOIC socket to 44 position J-leaded surface-mountable foot with test points. Pin mapping is 1:1

 $\underline{Tolerances:} \ diameters \ \pm 0.03 mm \ [\pm 0.001"], \ PCB \ perimeters \ \pm 0.13 mm \ [\pm 0.005"], \ PCB \ thicknesses \ \pm 0.18 mm \ [\pm 0.007"], \ pitches \ (from \ true \ position)$ ±0.08mm [±0.003"], all other tolerances ±0.13mm [±0.005"] unless stated otherwise. Materials and specifications are subject to change without notice.

CA-SO44A-Z-J-T-01F Drawing	Status: Released	Scale	: 2:1	Rev: A
© 2007 IRONWOOD ELECTRONICS, INC. 11351 Rupp Drive, Suite 400, Burnsville, MN 55337 Tele: (952) 229-8200 www.ironwoodelectronics.com	Drawing: H. Hansen		Date: 1/10/06	
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