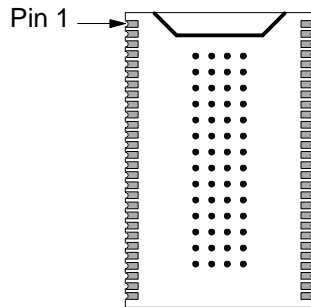


Top View




Orient Pin 1 of foot with respect to pin one of SOIC

Description: Map

Pin 1 of the SOIC package is connected to pin 1 of the SOIC surface mountable foot.

All tolerances: $\pm 0.005''$ (unless stated otherwise). Materials and specifications are subject to change without notice.

CA-SO56B-L-Z-01 Map	Status: Released	Scale 1.6:1	Rev: A
 <p>© 1999 IRONWOOD ELECTRONICS, INC. PO BOX 21151 ST. PAUL, MN 55121 Tele: (651) 452-8100 www.ironwoodelectronics.com</p>	Drawing: E Smolentseva	Date: 11/23/99	
	File: CA-SO56B-L-Z-01 Map	Modified:	