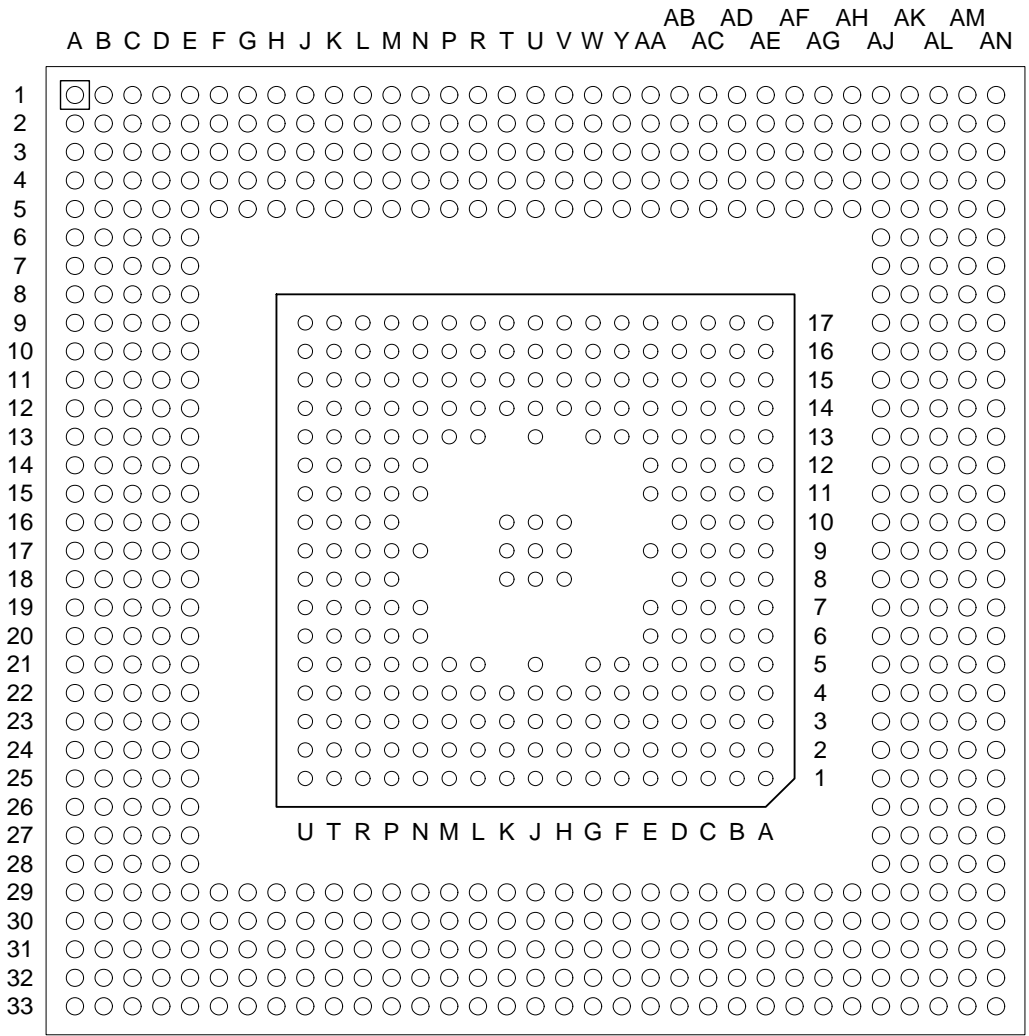




## DC-BGA/BGA-82801-S-B-01 Map Rev A, 2/2/01





DC-BGA/BGA-82801-S-B-01 Map  
Rev A, 2/2/01

**Nelist:**

Signal	BGA241	BGA560
+3.3V_STBY	L1, N1	J29, J30
5V_REF	C15	J31
A20GATE	B15	AF30
A20M#	F13	AK21
AC_BITCLK	R3	AJ1
AC_RST#	T1	AE1
AC_SDIN0	U1	D5
AC_SDIN1	P3	E6
AC_SDOOUT	T2	E29
AC_SYNC	T3	AH1
AD0	G2	AC31
AD1	G4	AC33
AD2	F2	AB30
AD3	F3	AB31
AD4	F4	AA29
AD5	F5	AA30
AD6	E1	AA31
AD7	E2	AA32
AD8	D1	Y29
AD9	D3	AA33
AD10	E4	Y30
AD11	C2	Y32
AD12	C1	R33
AD13	B1	R31
AD14	D4	R30
AD15	C3	R29
AD16	A4	P32
AD17	B4	P31
AD18	C5	P30
AD19	C6	P29
AD20	B5	M32
AD21	E7	N31
AD22	A6	N30
AD23	B6	L33
AD24	D7	M31
AD25	B8	L32
AD26	A7	M30
AD27	A8	L31

Signal	BGA241	BGA560
AD28	B7	M29
AD29	C9	J33
AD30	D8	L30
AD31	C7	K31
APICCLK	C16	C4
APICD0	E16	K3
APICD1	C17	E4
C_BE#0	D2	W31
C_BE#1	B2	W33
C_BE#2	A3	V30
C_BE#3	D6	V29
CLK14	U6	AK25
CLK48	U2	AL25
CLK66	A16	AL17
CPUSLP#	E12	AM23
DEVSEL#	D9	T30
FERR#	F15	AM20
FRAME#	B3	U33
GNT#0	A13	T32
GNT#1	C13	E33
GNT#2	A12	C33
GNT#3	C12	G32
GNT#4	A11	AK18
GNT#A	P5	AL23
GNT#B	R5	AL21
GPIO5	D11	C28
GPIO6	E11	A28
GPIO7	E9	C29
GPIO10	J4	AK24
GPIO12	N4	B29
GPIO13	L2	D30
GPIO21	B14	C30
GPIO22	D13	B30
GPIO23	D15	A31
GPIO25	L4	AM24
GPIO26	K4	P3
GPIO27	M5	W4
GPIO28	L5	AB5



DC-BGA/BGA-82801-S-B-01 Map  
Rev A, 2/2/01

Signal	BGA241	BGA560
HCOMP	M17	AE30
HL0	D17	AM12
HL1	E17	AM16
HL2	F17	AK14
HL3	G16	AN15
HL4	J15	AN9
HL5	K16	AN6
HL6	K17	AN7
HL7	L17	AM5
HL8	H15	AN13
HL9	J17	AM10
HL10	J14	AM8
HL11	F16	AM18
HLSTB	G17	AM17
HLSTB#	H17	AN17
HUBREF	J13	AK13, AL7, AL9, AL10, AL16, AM4, AM14, AN3
IGNNE#	B17	AN21
INIT#	E15	AJ21
INTR	E14	AJ22
IRDY#	A2	U31
IRQ14	P11	AJ19
IRQ15	N14	AL19
LAD0	R6	AM31
LAD1	U5	AN31
LAD2	T5	AL30
LAD3	T4	AM30
LDRQ#0	T6	AN29
LDRQ#1	N3	AL28
LFRAME#	U4	AM29
NMI	B16	AN19
OC#0	M3	AE3
OC#1	M4	AF2
PAR	A9	W29
PCICLK	C14	A17
PCIRST#	J5	L29
PDA0	R12	U1
PDA1	T12	T5
PDA2	P12	T4

Signal	BGA241	BGA560
PDCS#1	N12	V4
PDCS#3	U13	V5
PDD0	R10	AD3
PDD1	N9	AC3
PDD2	R9	AB4
PDD3	U9	AC1
PDD4	R8	AA5
PDD5	U8	AB3
PDD6	R7	AA4
PDD7	U7	AA3
PDD8	P7	Y5
PDD9	N7	AA1
PDD10	T8	Y4
PDD11	P8	Y3
PDD12	T9	R3
PDD13	P9	R4
PDD14	T10	R5
PDD15	P10	P2
PDDACK#	U12	T3
PDDREQ	U11	F1
PDIOR#	R11	G1
PDIOW#	T11	R1
PIORDY	N11	H2
PIRQ#A	D10	V32
PIRQ#B	A10	AE33
PIRQ#C	B10	AG33
PIRQ#D	C10	AH33
PLOCK#	B9	K30
PME#	K1	AL33
PWRBTN#	M2	K29
PWROK	J3	AL26
RCIN#	A15	AF29
REQ#0	A14	T31
REQ#1	B13	H33
REQ#2	B12	F33
REQ#3	D12	G29
REQ#A	N6	AJ23
REQ#B	P4	AM22
REQ4#	B11	AL18
RI#	L3	AD29



DC-BGA/BGA-82801-S-B-01 Map  
Rev A, 2/2/01

Signal	BGA241	BGA560
RSMRST#	F1	AD30
RTC_VCC	G1	H31
RTCST#	H1	AJ26
RTCX1	H3	AJ27
RTCX2	H4	AK27
SDA0	M16	U3
SDA1	M15	U4
SDA2	L13	T2
SDCS#1	L14	V3
SDCS#3	L16	W1
SDD0	P15	P4
SDD1	R16	P5
SDD2	T17	N2
SDD3	U16	N3
SDD4	U15	N4
SDD5	R14	M2
SDD6	P13	N5
SDD7	T13	M4
SDD8	U14	L1
SDD9	T14	L3
SDD10	P14	M5
SDD11	T15	K2
SDD12	U17	L5
SDD13	R15	J2
SDD14	R17	K4

Signal	BGA241	BGA560
SDD15	P16	J3
SDDACK#	M13	W5
SDDREQ	P17	C1
SDIOR#	N16	W3
SDIOW#	N15	V2
SERIRQ	R4	L4
SERR#	A1	V31
SIORDY	N17	U2
SLP_S3	K3	AK22
SLP_S5	K2	AK23
SMBALERT	M1	AN26
SMBCLK	J2	AM27
SMBDATA	J1	AK26
SMI#	F14	AJ20
SPKR	U3	B33
STOP#	D5	U32
STPCLK#	A17	AJ24
THRM#	D14	AK19
TRDY#	C4	U29
USBP0N	N2	AG2
USBP0P	P1	AJ2
USBP1N	P2	AL1
USBP1P	R1	G4
VBIAS	H2	AN28

### Signal Planes:

**+1.8V:** G15, H14, H16, J16, K14, L15, AM2, AM15, AN4, AN8, AN12, G13

**+3.3V:** C8, A5, C11, D16, E3, E5, E6, E13, G5, M14, N5, N13, P6, R13, T7, T16, U10, A6, A10, A16, A19, A22, A26, A30, B2, B13, B19, B32, C3, C32, D1, D7, D10, D11, D13, D16, D20, D26, D29, D33, E5, E7, E15, E21, E23, E24, E27, H1, K33, M1, N32, R2, T33, V1, W32, AA2, AB33, AD1, AF33, AJ18, AJ25, AK1, AK28, AK33, AL2, AL20, AL24, AL29, AL31, AM21, AM26, AM32, AN18, AN23, AN24, AN30

**GND:** H8, G14, H9, H10, J8, J9, J10, K8, K9, K10, K15, R2, A1, A7, A12, A14, A18, A20, A24, A29, A32, A33, B1, B6, B9, B15, B23, B27, B31, C2, E1, F32, G2, G33, J32, K1, L2, M33, P1, P33, R32, T1, V33, W2, Y1, Y33, AB1, AC32, AD33, AE2, AG1, AG32, AH2, AJ33, AL32, AM3, AM7, AM11, AM19, AM25, AM28, AM33, AN1, AN2, AN5, AN10, AN14, AN16, AN20, AN22, AN27, AN33