Ironwood Electronics, Inc.

Model P-P115A

Report summary sheet

**Pin test configuration:**

12 pins in a 0.5mm pitch array, two signal pins surrounded by ground. Delrin pin block 0.5” x 1.2, $\varepsilon = 3.6$, 0.140” thick

**Current carrying capacity: 2.5A**

**Performance:**

**Time domain:**
- Signal delay = 28 ps
- Rise time, open circuit < 26 ps
- Rise time, short circuit < 60 ps
- Rise time, thru 50 O < 27.6 ps
- Pin to pin crosstalk < 7 %
  (25 ps RT, thru 50 O)

**Frequency domain:**
- Insertion loss < 0.25 dB to 4.5 GHz
  < 3 dB to 10 GHz
- VSWR < 1:1.8 to 10 GHz
- S31 (crosstalk) < -15 dB to 6.5 GHz

**Equivalent circuit parameters:**
- Pin inductance = 1.3 nH
- Mutual inductance = 0.15 nH
- Pin to ground capacitance = 0.53 pF
- Pin to pin capacitance = 0.045 pF
- Transmission line = $Z_0 = 45$ ohms, $T_i = 27$ ps