

SG-BGA-7042 Drawing		Status: Released	Scale: -		Rev: B	All tolerances: ±0.12 otherwise). Materia	
	© 2005 IRONWOOD ELECTRONICS, INC. Drawing: H. Hansen Date: 3/17/05		7/05	are subject to chang			
	Tele: (952) 229-8200 www.ironwoodelectronics.com	File: SG-BGA-7042 Dwg	File: SG-BGA-7042 Dwg		Modified: 6/2/09, AE		

Il tolerances: ±0.125mm (unless stated therwise). Materials and specifications re subject to change without notice.

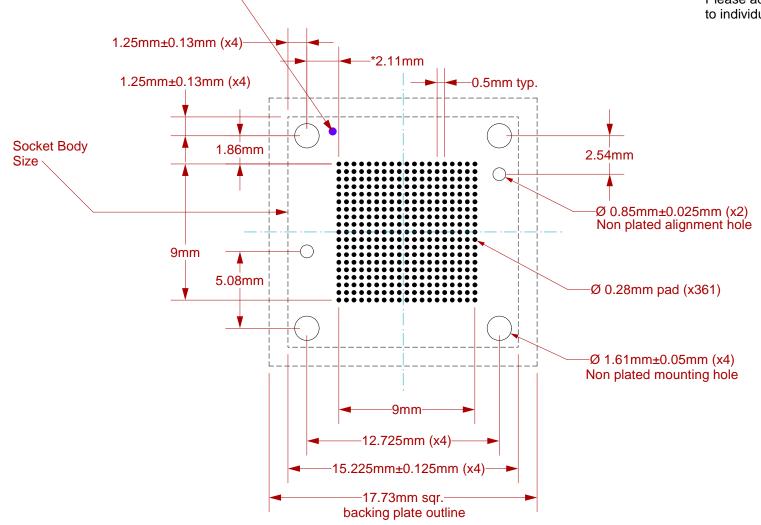
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Recommended PCB Layout Top View

Orientation Mark

*Note: BGA pattern is not symmetrical with respect to the mounting holes.

Note: Full BGA pattern shown. Please adjust pattern according to individual requirements.



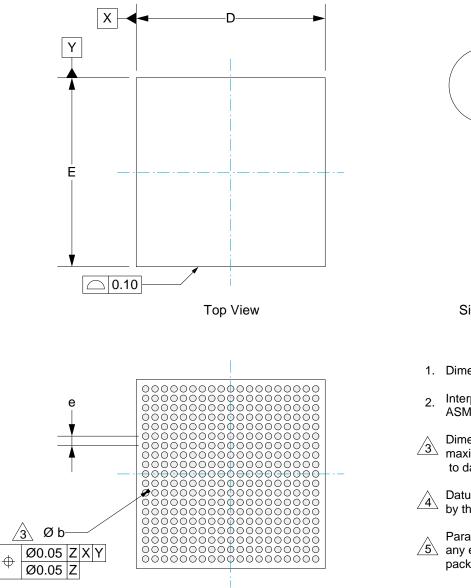
Target PCB Recommendations

Total thickness: 1.6mm min. Plating: Gold or Solder finish PCB Pad height: Same or higher than solder mask All dimensions are in mm unless stated otherwise

Recommended PCB Layout Tolerances: ±0.025mm [±0.001"] unless stated otherwise.

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∕5∖ DETAIL // 0.10 Z А Ζ ⊇ 0.08 Z ∕4∖ A1

Side View

b

b

D

b b

b Б Б Ď Б

- 1. Dimensions are in millimeters.
- Interpret dimensions and tolerances per ASME Y14.5M-1994.

Detail

- Dimension b is measured at the maximum solder ball diameter, parallel to datum plane Z.
- Datum Z (seating plane) is defined by the spherical crowns of the solder balls.
- Parallelism measurement shall exclude 15 any effect of mark on top surface of package.

DIM	MIN	MAX		
A		1.2		
A1	0.13	0.27		
b		0.35		
D	10.0 BSC			
E	10.0 BSC			
е	0.5 BSC			

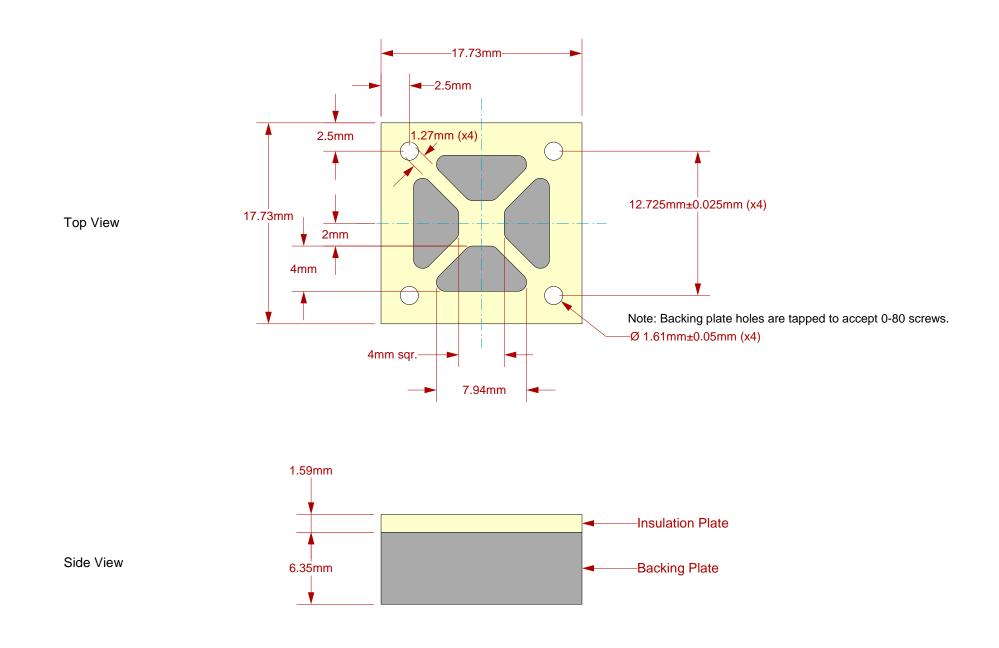
19x19 array





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Description: Insulation Plate and Backing Plate

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All dimensions are in mm. All tolerences are +/- 0.125mm. (Unless stated otherwise)

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